

Notice of Allowability	Application No.	Applicant(s)	
	10/070,006	WOLRICH ET AL.	
	Examiner B. James Peikari	Art Unit 2189	

-- *The MAILING DATE of this communication appears on the cover sheet with the correspondence address--*

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. This communication is responsive to the RCE filed on July 27, 2006.
2. The allowed claim(s) is/are 26-35.
3. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) All
 - b) Some*
 - c) None
 of the:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) hereto or 2) to Paper No./Mail Date _____.
 - (b) including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. Notice of References Cited (PTO-892)
2. Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date 7/27/06
4. Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. Notice of Informal Patent Application (PTO-152)
6. Interview Summary (PTO-413),
Paper No./Mail Date _____.
7. Examiner's Amendment/Comment
8. Examiner's Statement of Reasons for Allowance
9. Other Appendix A.

EXAMINER'S COMMENT

Information Disclosure Statement

1. The following references cited on the information disclosure statement filed July 27, 2006 have not been considered because they fail to comply with the provisions of 37 CFR 1.97, 1.98 and MPEP § 609 because:

- (A) the reference JP 59111533 (i.e., Reference "AU" on Sheet 1 of 3) is not in accordance with 37 CFR 1.98(a)(3)(i). It has no proper concise explanation of relevance because the English language abstract has been translated in a manner replete with errors to the extent that it is confusing and cannot be properly understood. Note Appendix A attached hereto.
- (B) the reference WO 01/15718 (i.e., Reference "AM" on Sheet 1 of 3) is not relevant.
- (E) the reference 5,023,023 (i.e., Reference "AA" on Sheet 1 of 2) is not listed in accordance with 37 CFR 1.98(b)(1) because the patent number does not match the inventor and issue date. Presumably, the reference should be listed as "4,023,023".

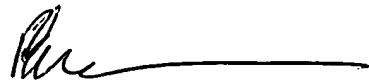
Applicant is advised that the date of any re-submission of any item of information contained in this information disclosure statement or the submission of any missing element(s) will be the date of submission for purposes of determining compliance with the requirements based on the time of filing the statement, including all certification requirements for statements under 37 CFR 1.97(e). See MPEP § 609.05(a).

In the interest of compact prosecution, applicant is requested to refrain from submitting duplicate listings of references that have already been considered.

2. Any inquiry concerning this communication or earlier communications from the examiner should be directed to James Peikari whose telephone number is (571) 272-4185. The examiner is generally available between 7:00 am and 7:30 pm, EST, Monday through Wednesday, and between 5:30 am and 4:00 pm on Thursday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Reginald Bragdon, can be reached at (571) 272-4204. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center at 866-217-9197 (toll-free).



B. James Peikari
Primary Examiner
Art Unit 2189
8/15/06

APPENDIX A

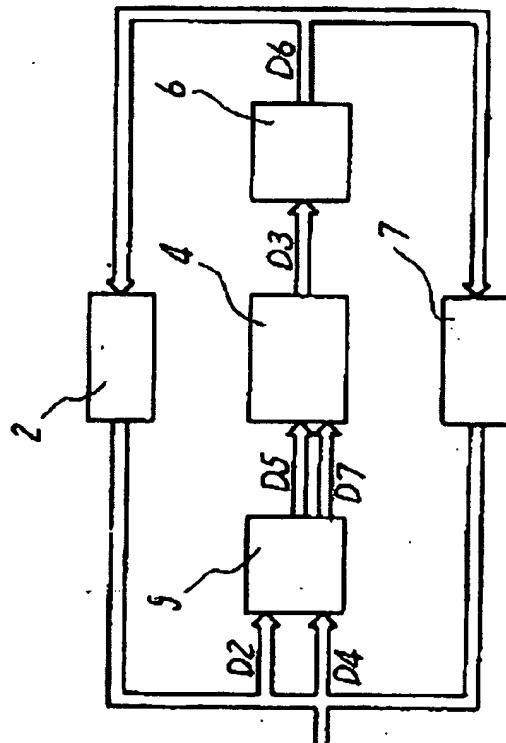
DIGITAL DATA ARITHMETIC CIRCUIT

Patent number: JP59111533
Publication date: 1984-06-27
Inventor: ABE TADASHI; others: 01
Applicant: HITACHI SEISAKUSHO KK
Classification:
 - **international:** G06F7/38; G06F15/20
 - **european:**
Application number: JP19820220255 19821217
Priority number(s):

Abstract of JP59111533

PURPOSE: To accelerate the arithmetic speed by dividing a memory part into plural groups to write the data read out by a command to the memory part of a group and the data on the arithmetic result to the memory part of the other group respectively.

CONSTITUTION: The data read out by a command is written to a memory part of one of plural divided groups of memories along with the data on arithmetic result written to a memory of the other group respectively. Thus the data is read and written at a time. For instance, the data D2 is read out of a designated address of a memory element 2 and held at an input holding part 5 together with the input data D4 to be calculated. Then data D5 and D7 are supplied to an arithmetic device 4 to perform an operation. The data D3 calculated by the device 4 is held at an output holding part 6, and at the same time the device 4 is replaced for the next calculation. Then the data D6 on the arithmetic result held at the part 6 is stored in a memory element 7. Meanwhile the replaced data is supplied to the device 4 to give an operation to the next replaced data.



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